

FIG. 1

FIG. 2

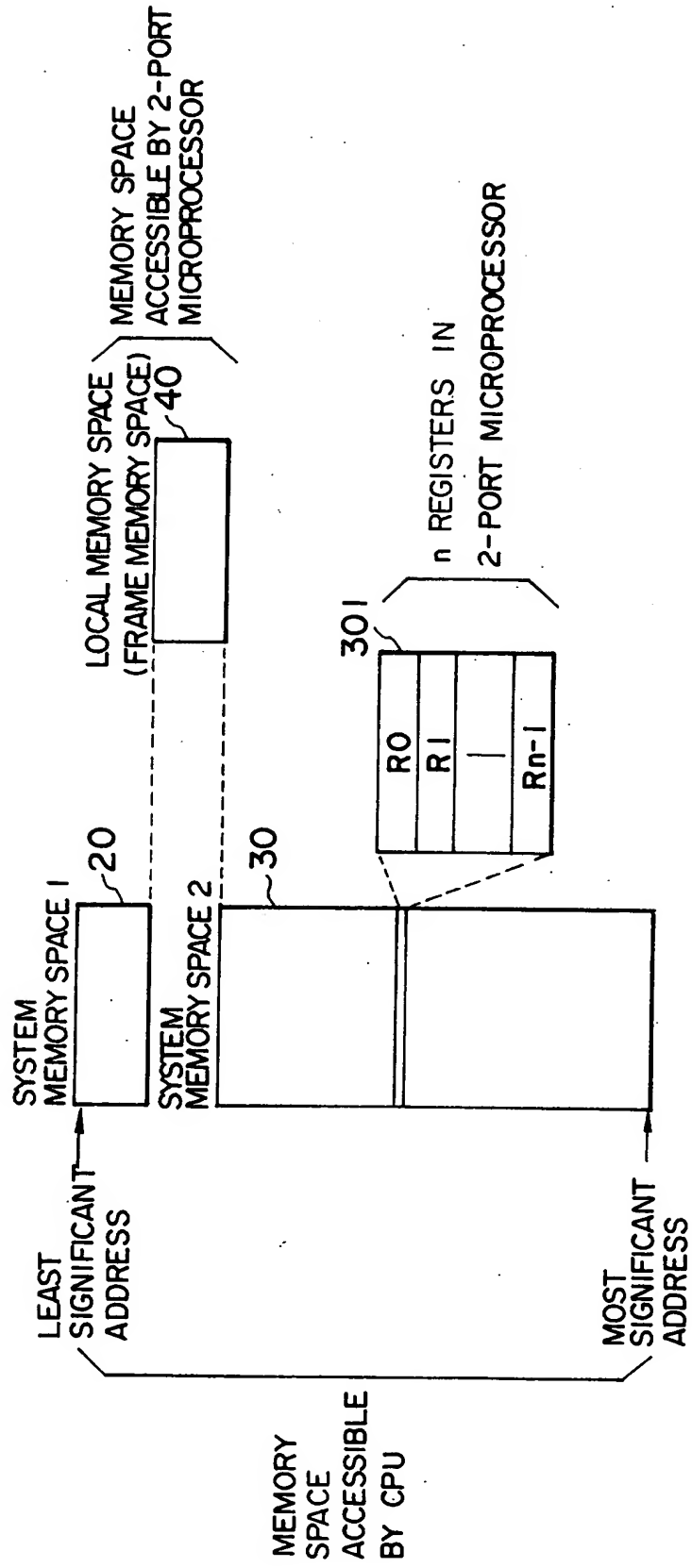


FIG. 3

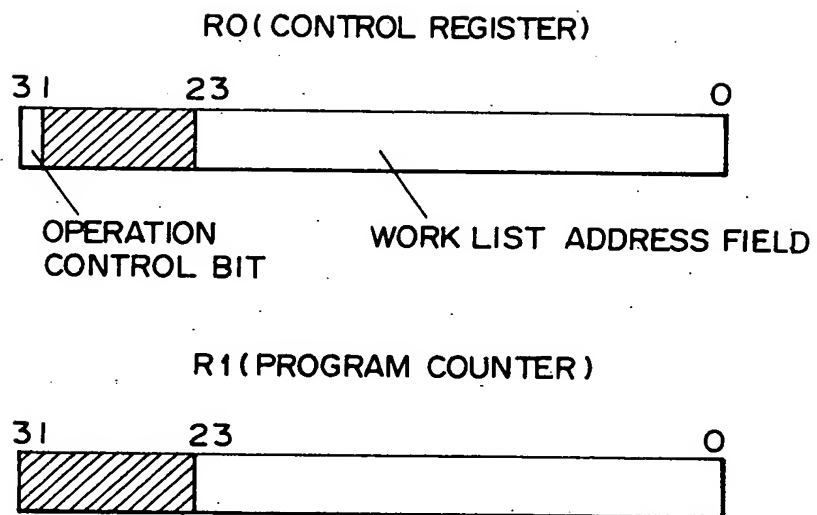


FIG. 4

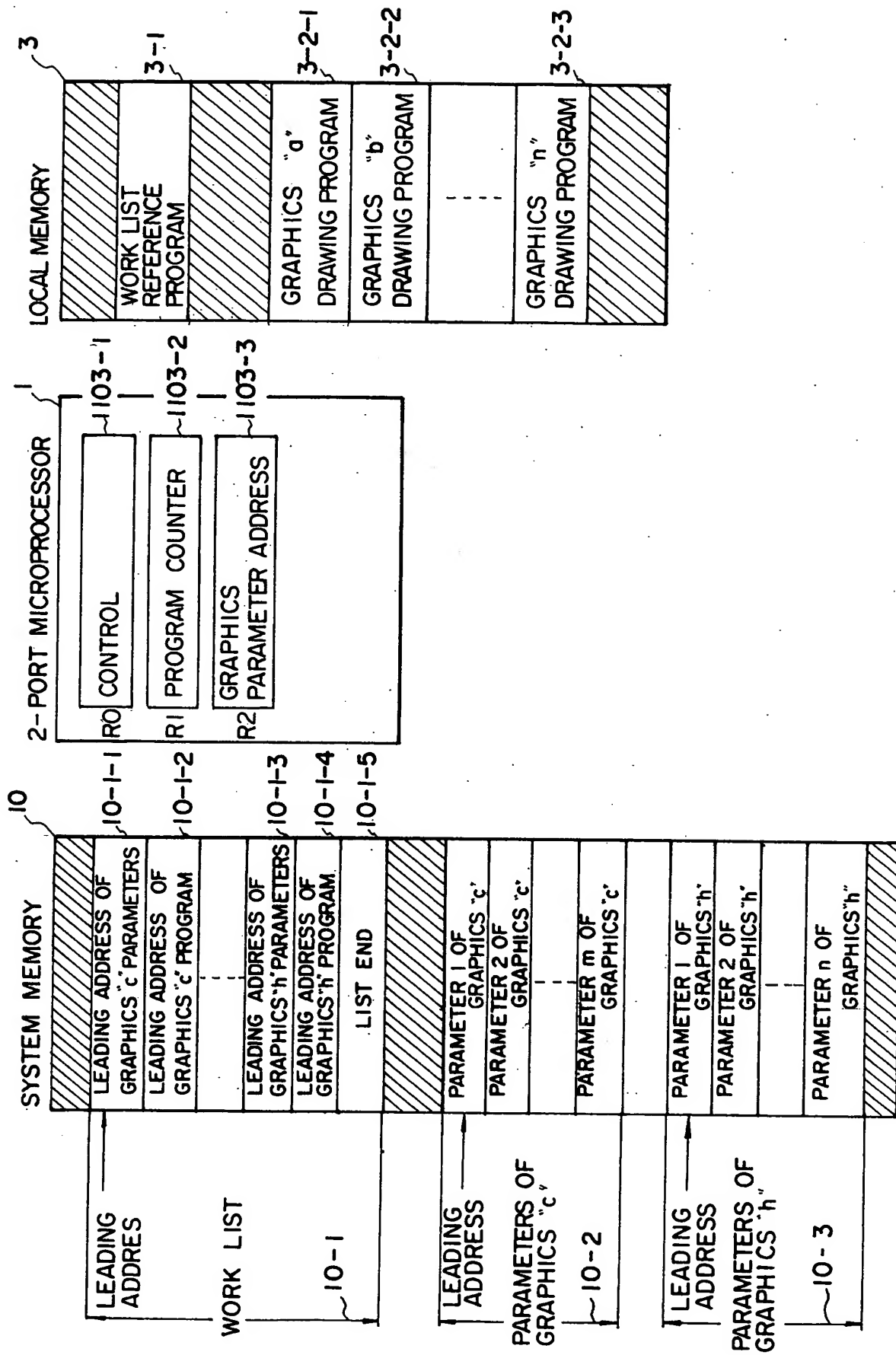


FIG. 5

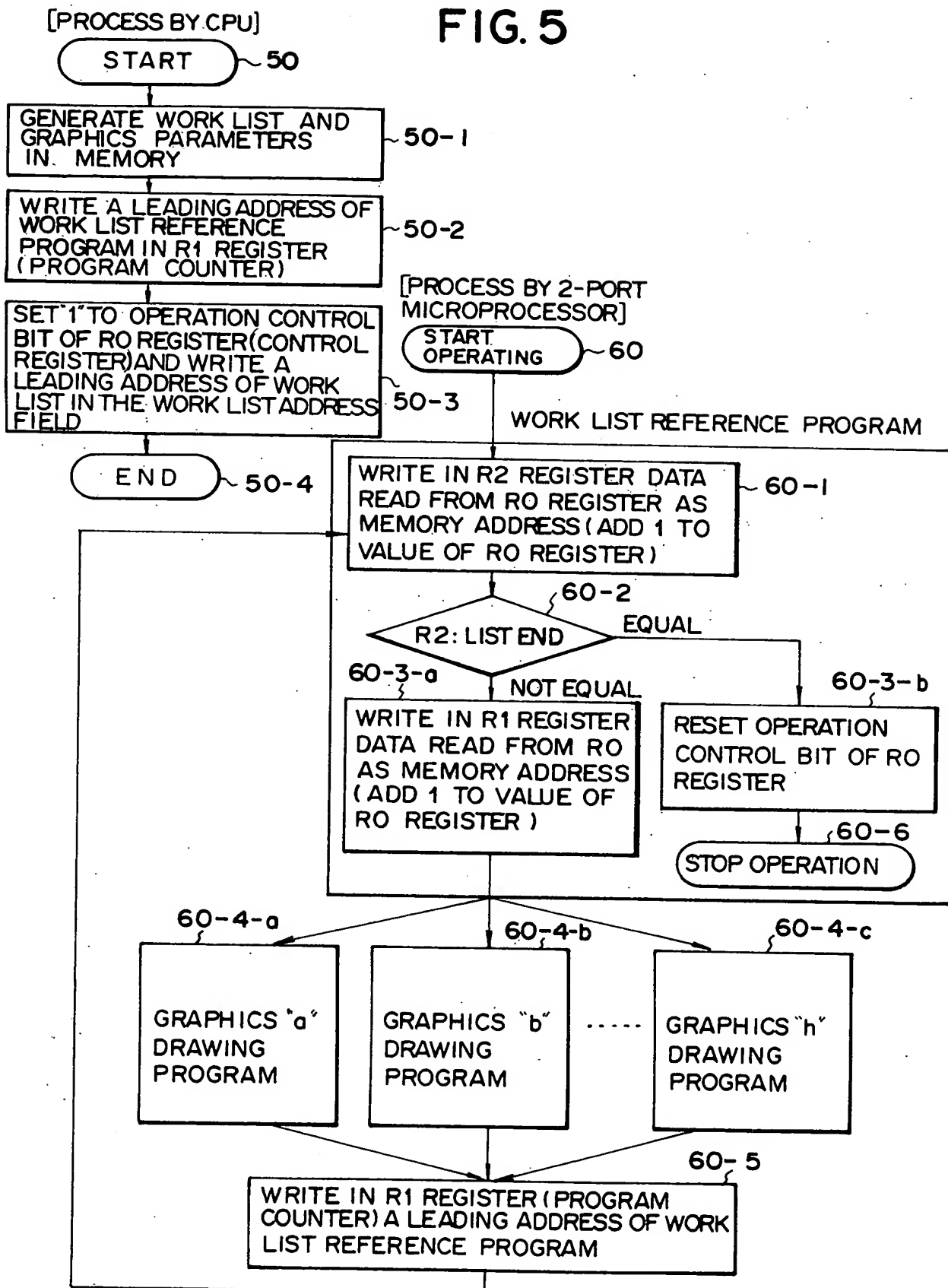


FIG. 6

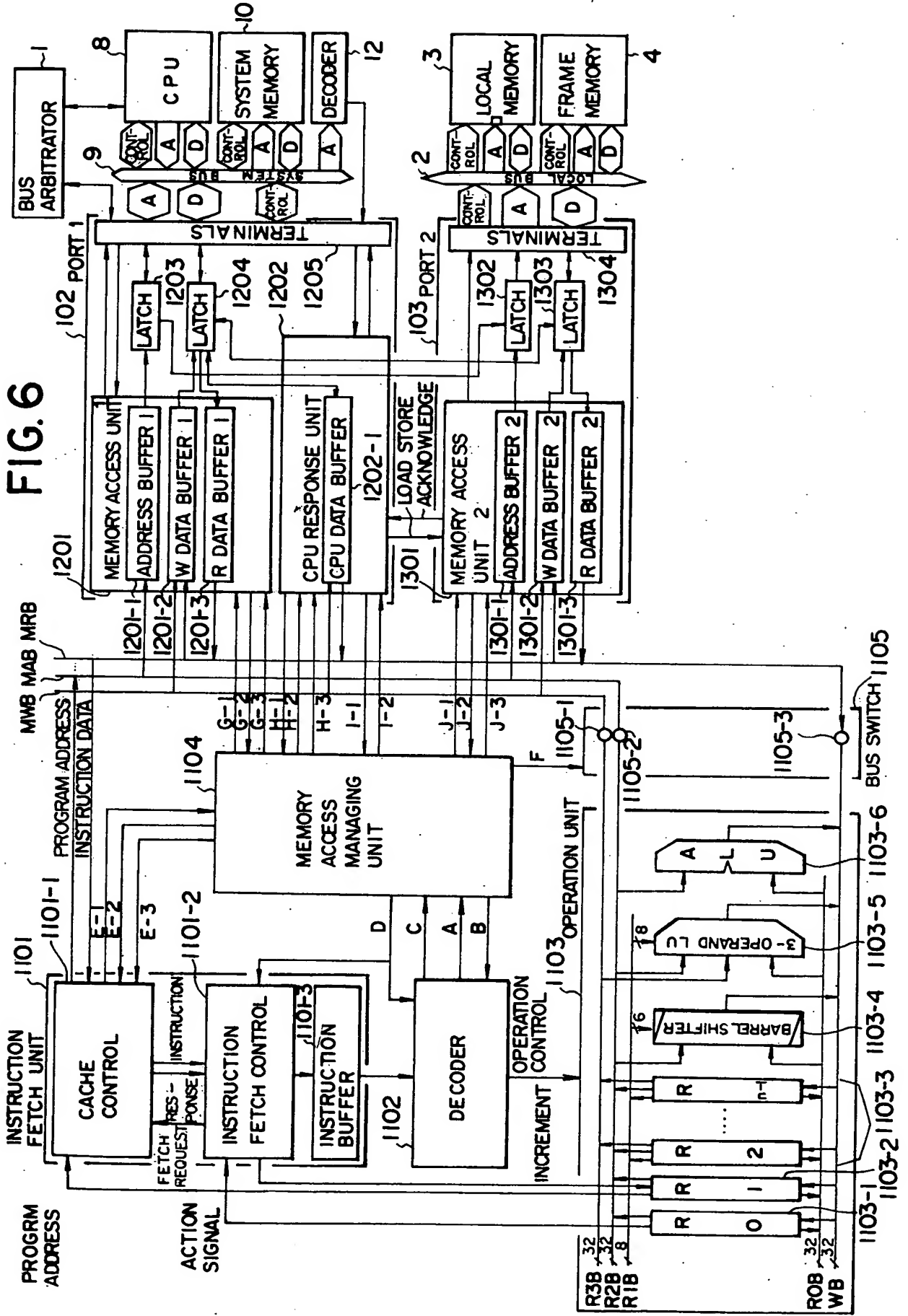


FIG. 7

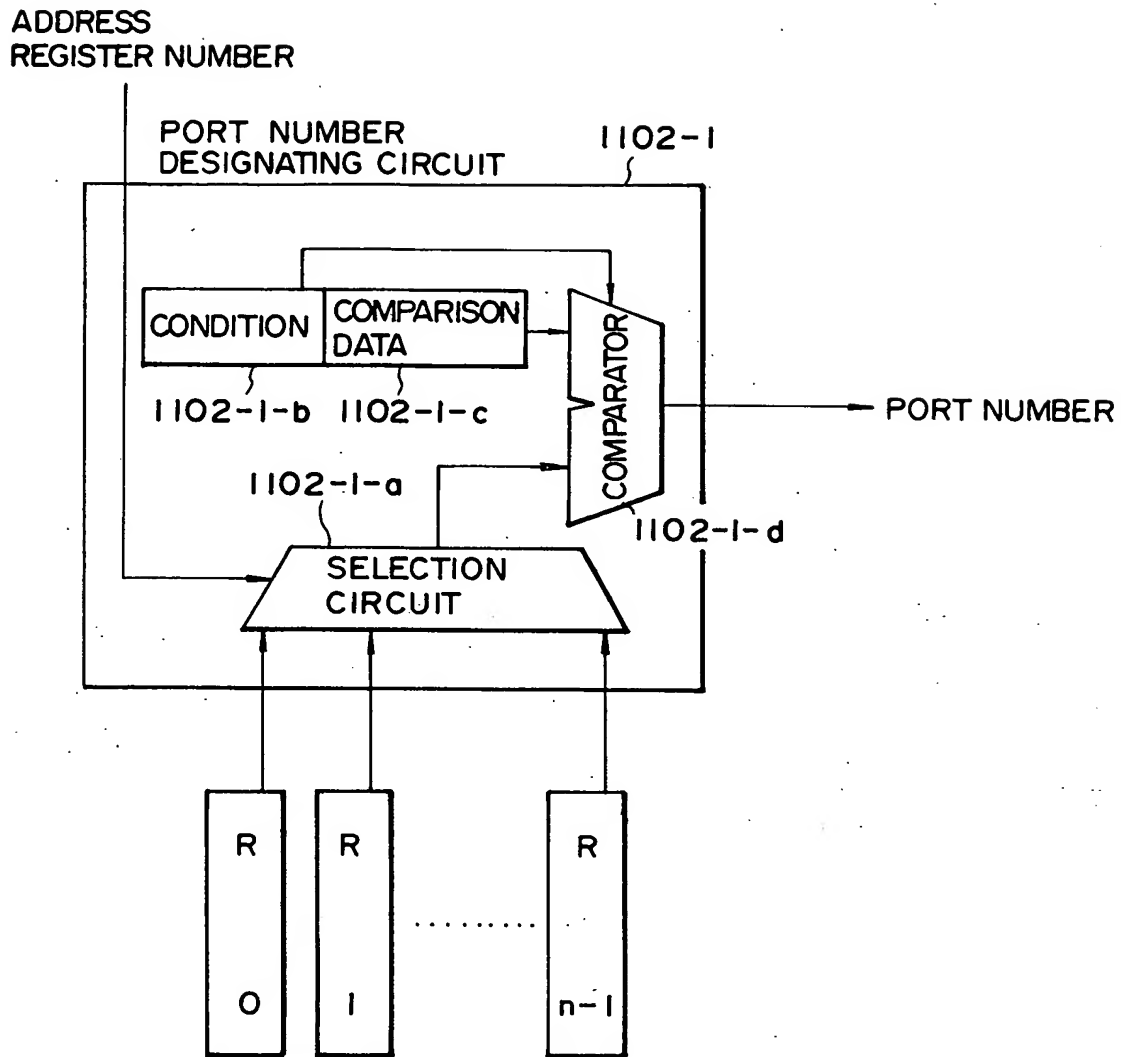


FIG. 8

PORT DESIGNATING METHOD	CONDITION BIT
IF DATA OF ADDRESS REGISTER \cong COMPARISON DATA, DESIGNATE PORT 1	0
IF DATA OF ADDRESS REGISTER \cong COMPARISON DATA, DESIGNATE PORT 2	1

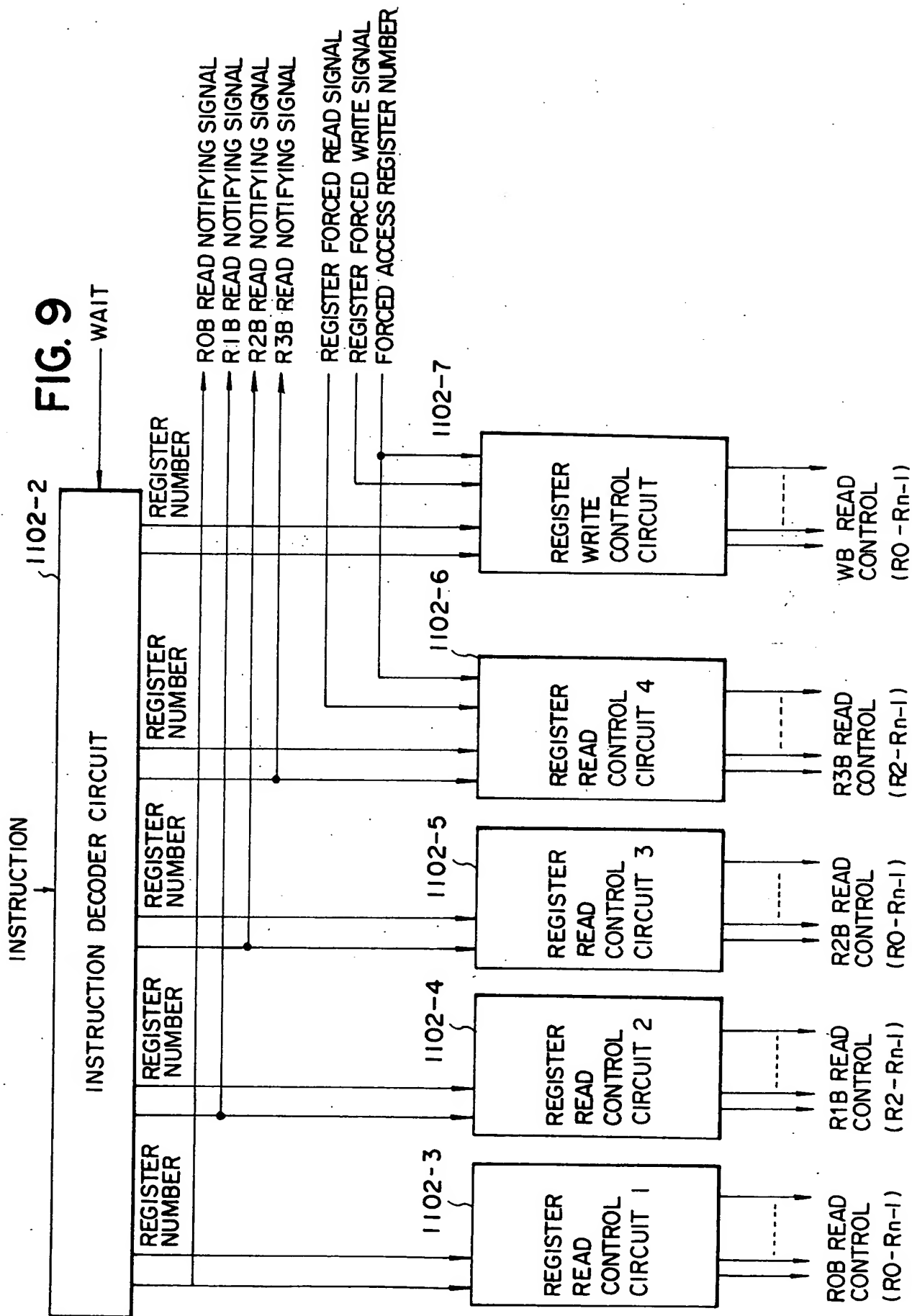


FIG. 10

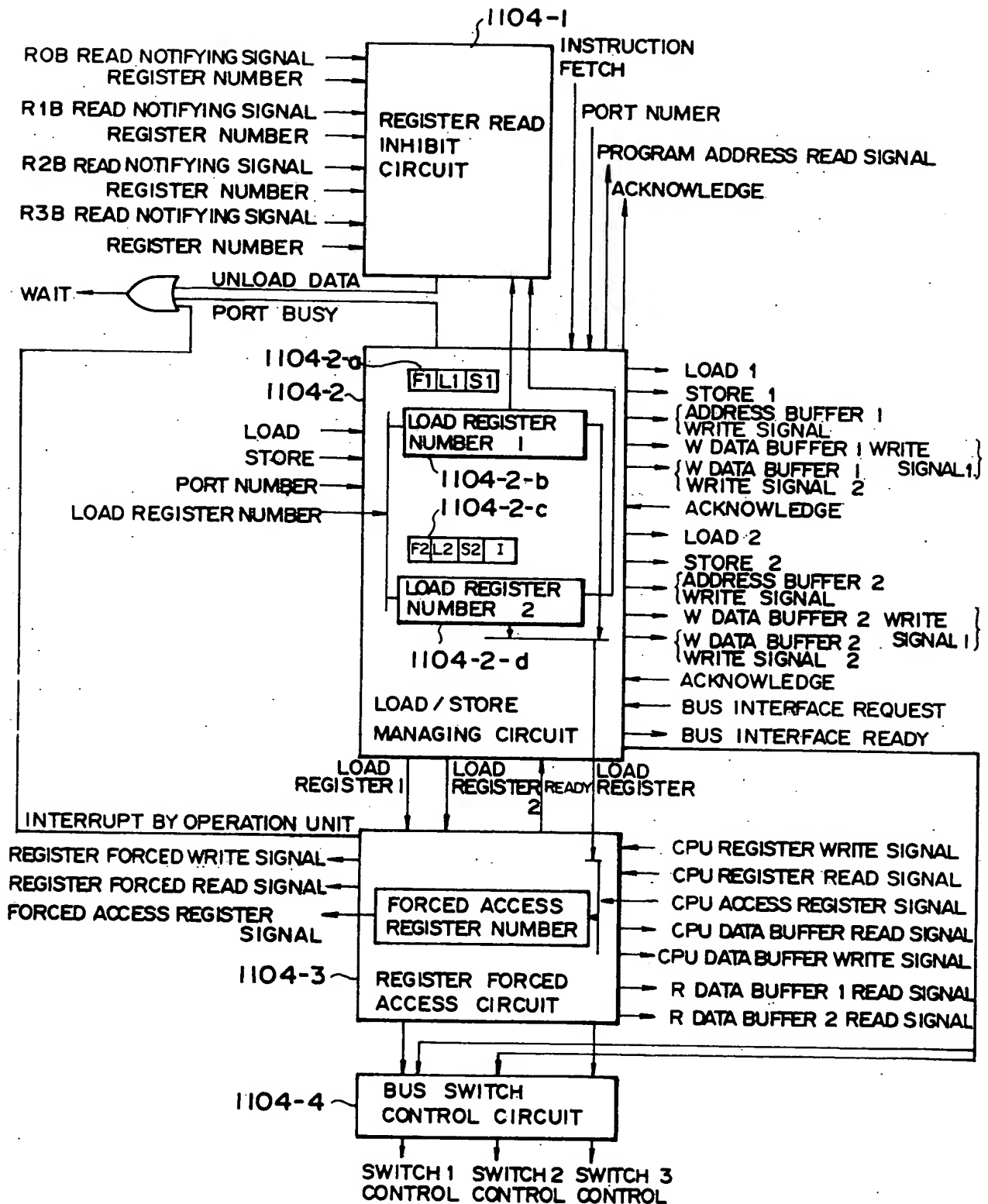


FIG. 11

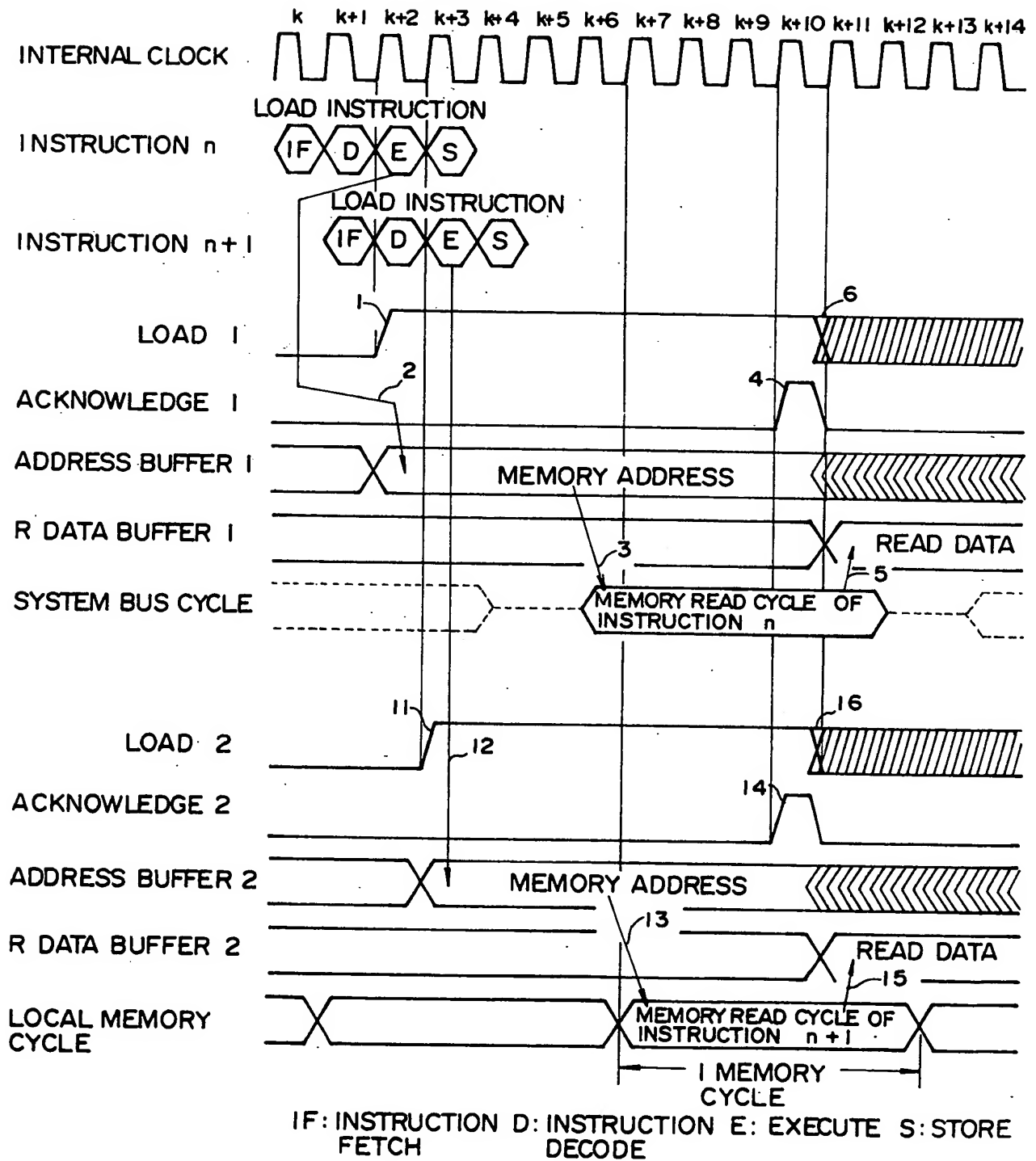
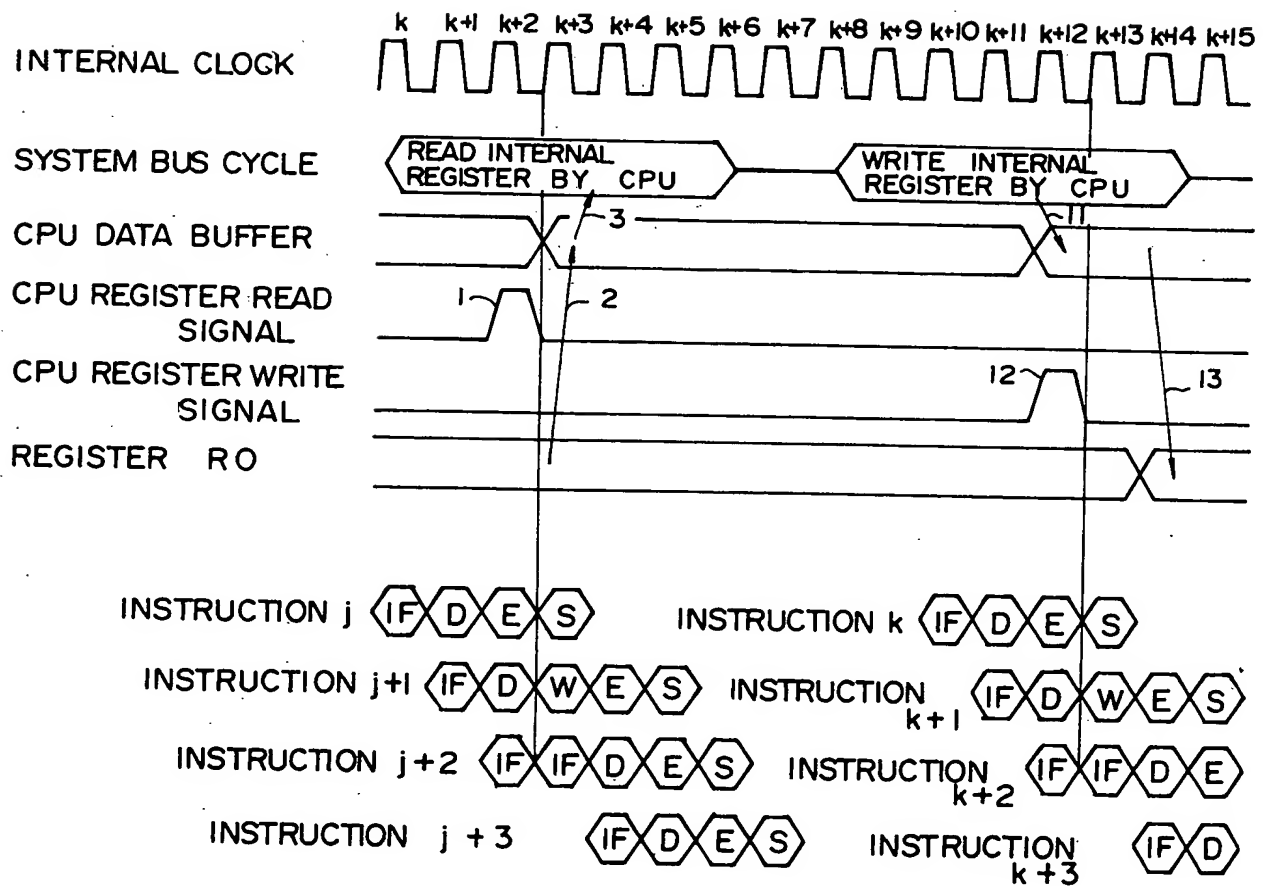


FIG. 12



IF: INSTRUCTION FETCH D: INSTRUCTION DECODE E: EXECUTE S: STORE W: WAIT

FIG. 13

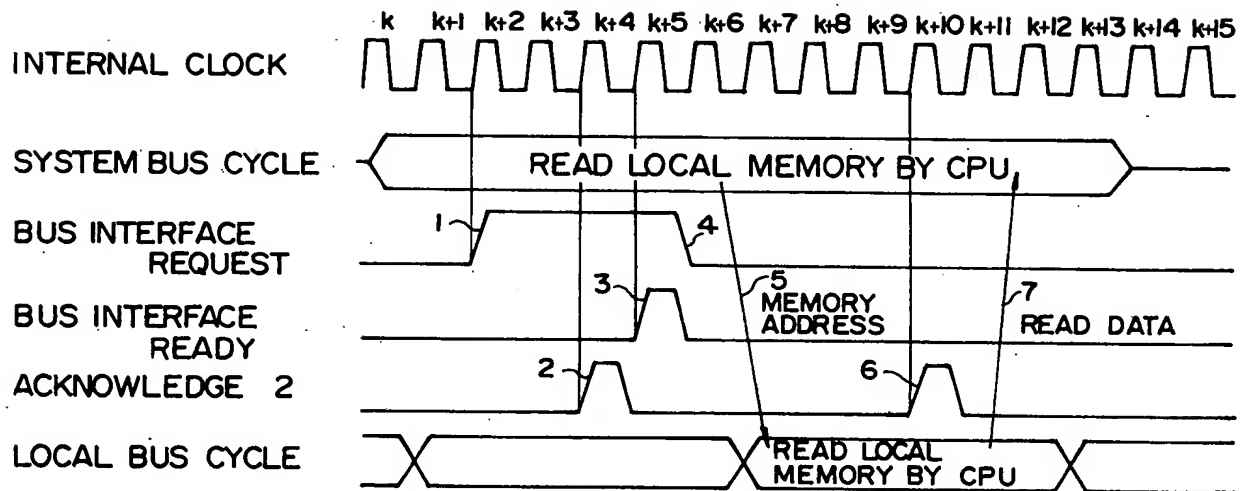


FIG. 14

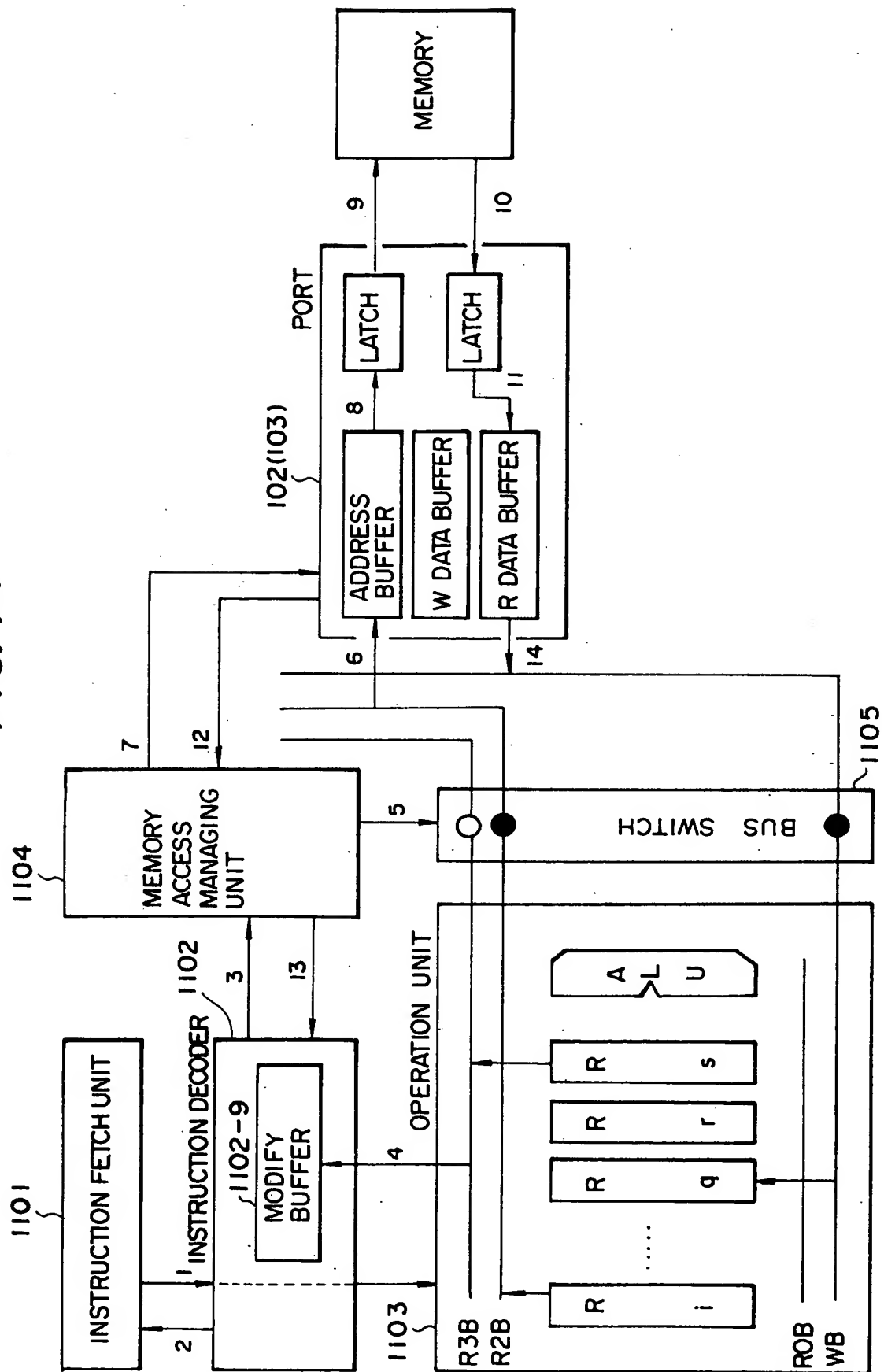


FIG. 15

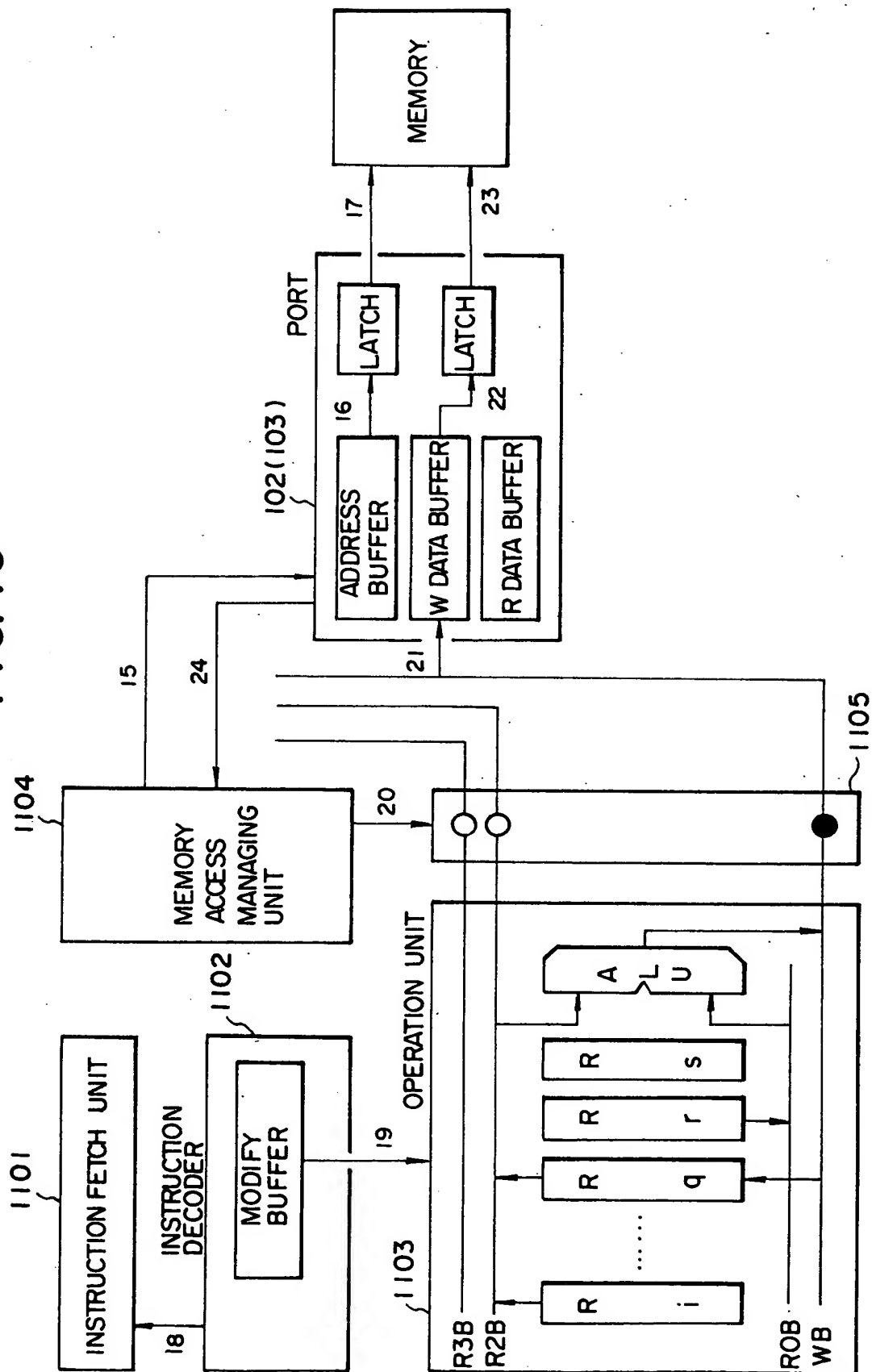


FIG. 16

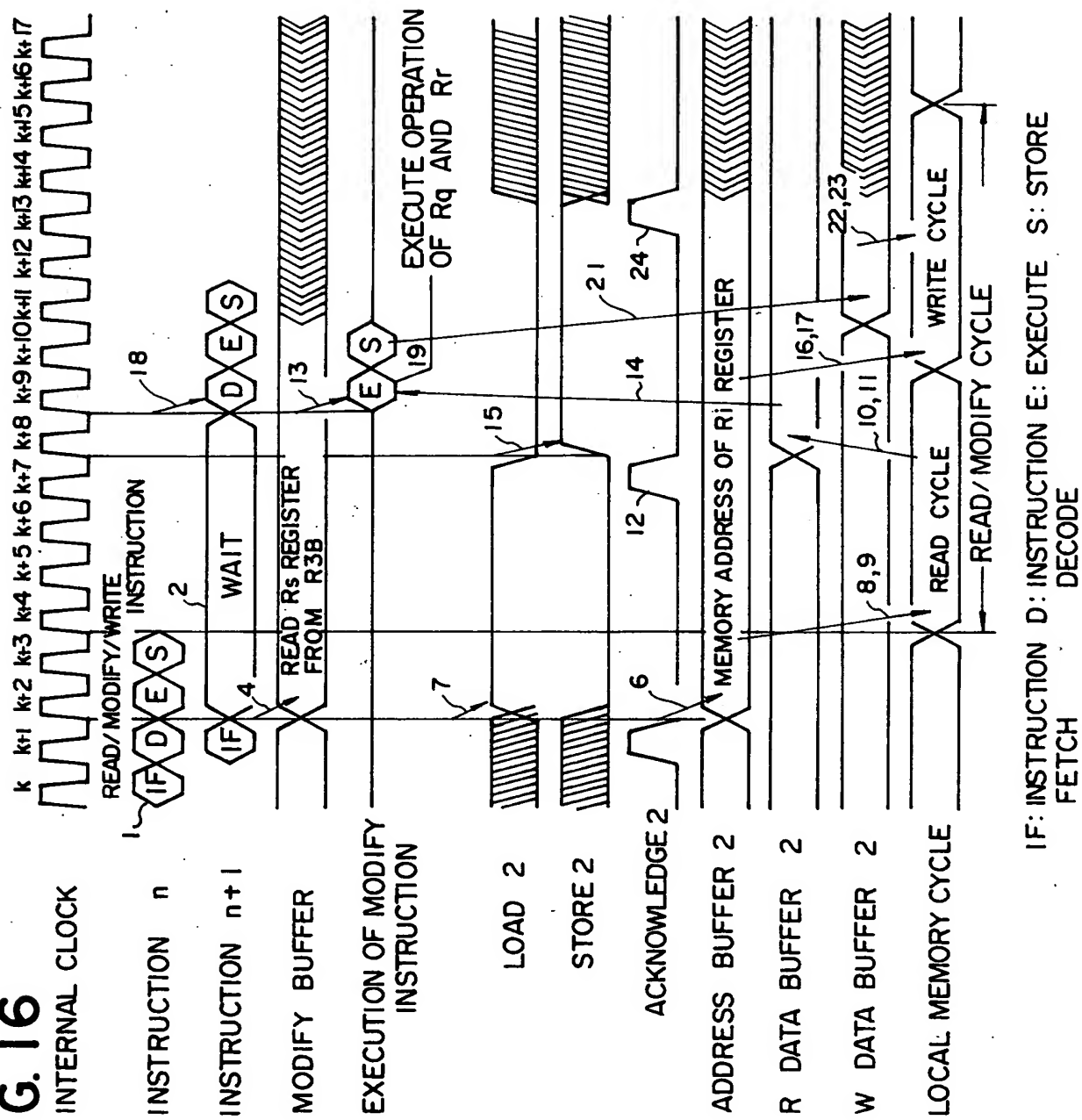


FIG. 17

2 - PORT MICROPROCESSOR

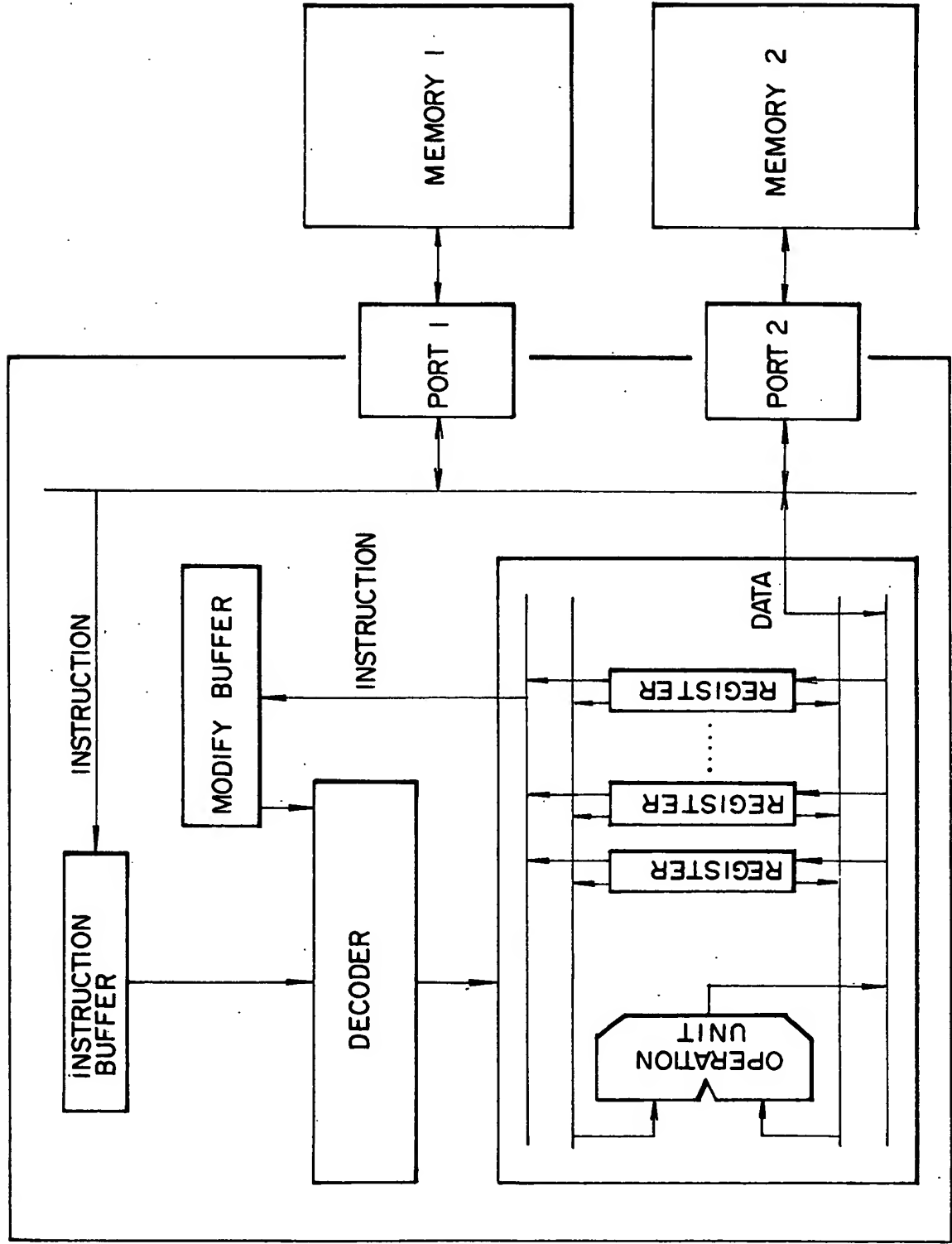


FIG. 18

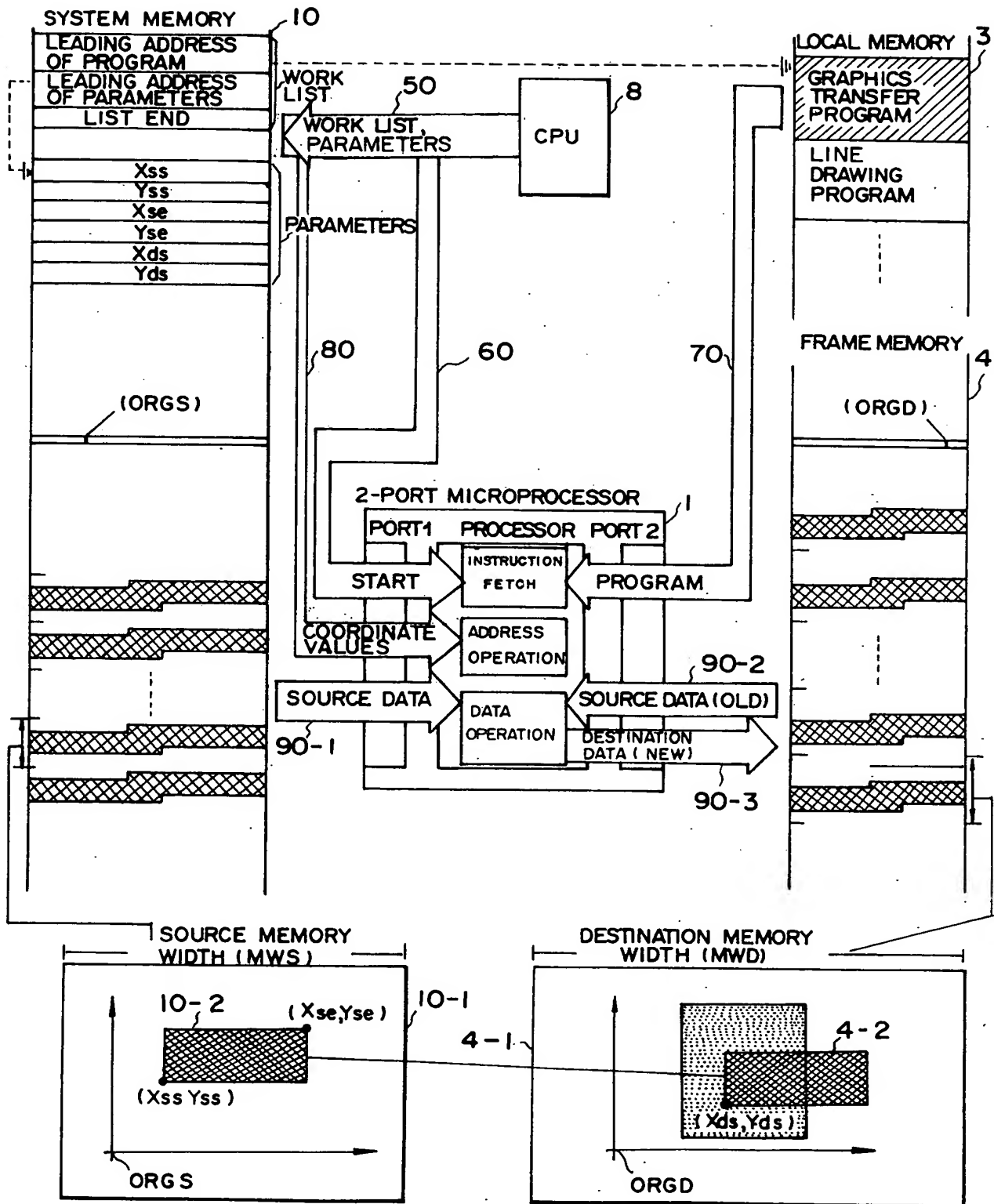


FIG. 19

